# Part 1\* - Positive Feedback and Relaxation Oscillators

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#### What do I work on? Nonlinear Dynamical Systems and Embedded Systems

 Physical Memristors: discharge tubes, PN junctions and Josephson Junctions (MSOE; IIT Chennai; Vellore Institute of Technology)
Applications and Mathematical properties of the Muthuswamy-Chua system (MSOE; Vellore Institute of Technology; University Putra Malaysia, Malaysia)
Applications of Chaotic Delay Differential Equations using Field Programmable Gate Arrays (FPGAs) (MSOE; Vellore Institute of Technology; University Putra Malaysia, Malaysia)
Pattern Recognition Using Cellular Neural Networks on FPGAs (MSOE; Vellore Institute of Technology; University of California, Berkeley; Altera Corporation)

#### Education

- Nonlinear Dynamics at the undergraduate level (with folks from all over the world  $\ensuremath{\textcircled{}}$  )





#### Professor : What are the applications of this research?

My answer : To quote Max Planck, "Scientific discoveries and inventions have been achieved only by those who went in pursuit of them without any applications in mind."

# My question : Who is Max Planck?



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#### Goal of this Lecture

Analyze and simulate circuit [4] shown below. Specifically, sketch Vo(t) in the circuit below. Note: You should realize circuit physically on a breadboard.



#### NOTE: When you have questions, please STOP ME and ASK!



# If you have seen this circuit before...





# Introduction : Fundamental Circuit Theory [1-3]



Memristors were first postulated by Leon. O Chua in 1971 [1]



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- 2. Concept of a linear time-invariant system
- 3. Various system behaviors: stable, unstable



## "Review" : The Operational Amplifier [5]



$$v_{o} = \begin{cases} -V_{CC} & A(v_{p} - v_{n}) < -V_{CC}, \\ A(v_{p} - v_{n}) & -V_{CC} \leq A(v_{p} - v_{n}) \leq +V_{CC}, \\ +V_{CC} & A(v_{p} - v_{n}) > +V_{CC}. \end{cases}$$
(5)



#### Step 1: Understand Problem





### Step 2: Devise a Plan

1. Derive i-v graph of  $N_R = I(V)$  using op-amp ideas from lecture 1

2. Use 
$$I = -C \frac{dV}{dt}$$
 (6)

(concepts from circuit theory)

3. Use (1) to find and sketch Vo(t)



# Step 3 (a): Carry out the plan



3. Recall three modes of operation for an op-amp : linear region, positive saturation and negative saturation.



# Step 3 (b): Carry out the plan

1. Op-amp in linear region 2. Circuit Equations

⊖Vss |Vss

Vdd

**∀**Vdd

R1 -//// 1kΩ U1A

AD712JN

Vo

Vn

Vp

٧

-Ļ 0

¦R2 ≷1kΩ









# Step 3 (c): Carry out the plan





# Step 3 (d): Carry out the plan









# Step 3(e) : Sketching Vo(t)





### Step 4: Check your answer









#### Remember us $\odot$ ?





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#### References

- 1. Chua, L. O. "Memristor The Missing Circuit Element". *IEEE Transactions on Circuit Theory*, Vol. CT-18, No. 5, pp. 507- 519. September 1971.
- 2. Chua, L. O. and Kang, S. M. "Memristive Devices and Systems". *Proceedings of the IEEE*, Vol. 64, No. 2, pp. 209-223. February 1976.
- 3. Chua, L. O., Desoer, C. A. and Kuh, E. H. "Linear and Nonlinear Circuits". McGraw-Hill, 1987.
- Kennedy, M. P. and Chua, L. O. "Hysteresis in Electronic Circuits : A Circuit Theorist's Perspective". International Journal of Circuit Theory and Applications, Vol. 19, pp. 471 – 515, 1991.
- 5. Nilsson, J. W. and Riedel, S. "Electric Circuits". 9<sup>th</sup> Edition, Pearson, 2011.

